

What is claimed is:

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1. A RAM-incorporated driver which drives a display section based on still-image data and moving-image data, the RAM-incorporated driver comprising:
- 5 a first port through which the still-image data or a given command is input;
- a second port through which the moving-image data, which is transferred serially over a serial transfer line, is input
- 10 as a differential signal;
- a reception circuit which differentially amplifies the differential signal input from the second port and creating the moving-image data in a parallel state;
- 15 a RAM which stores the still-image data that was input through the first port and the moving-image data that was created by the reception circuit;
- a first control circuit which controls writing or reading of the still-image data or the moving-image data that has been input separately through the first port or the
- 20 second port, with respect to the RAM; and
- a second control circuit independently of the first control circuit, which controls the reading as display data of the still-image data or moving-image data that has been stored in the RAM, and driving the display section to display.
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2. The RAM-incorporated driver as defined by claim 1; comprising:

a halt control circuit which receives with the differential signal a data validation signal indicating whether or not the differential signal is valid, and halting at least part of an operation of the reception circuit, based on the data validation signal.

3. The RAM-incorporated driver as defined by claim 2, wherein the validation signal is used as a synchronization signal synchronizing the writing of the moving-image data into the RAM.

4. The RAM-incorporated driver as defined by claim 2, wherein the validation signal is used as a synchronization signal synchronizing the writing of the moving-image data for one line of the display section into the RAM.

5. The RAM-incorporated driver as defined by claim 2, wherein the validation signal is used as a synchronization signal synchronizing the writing of the moving-image data for one full-screen of the display section into the RAM.

6. The RAM-incorporated driver as defined by claim 1, wherein the serial transfer line is a transfer line in accordance with an LVDS standard.

7. The RAM-incorporated driver as defined by claim 2,  
wherein the serial transfer line is a transfer line in  
accordance with an LVDS standard.

5 8. The RAM-incorporated driver as defined by claim 3,  
wherein the serial transfer line is a transfer line in  
accordance with an LVDS standard.

10 9. The RAM-incorporated driver as defined by claim 4,  
wherein the serial transfer line is a transfer line in  
accordance with an LVDS standard.

15 10. The RAM-incorporated driver as defined by claim 5,  
wherein the serial transfer line is a transfer line in  
accordance with an LVDS standard.

20 11. The RAM-incorporated driver as defined by claim 1,  
wherein the serial transfer line is a transfer line in  
accordance with a USB standard.

12. The RAM-incorporated driver as defined by claim 2,  
wherein the serial transfer line is a transfer line in  
accordance with a USB standard.

25 13. The RAM-incorporated driver as defined by claim 3,  
wherein the serial transfer line is a transfer line in  
accordance with a USB standard.

14. The RAM-incorporated driver as defined by claim 4,  
wherein the serial transfer line is a transfer line in  
accordance with a USB standard.

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15. The RAM-incorporated driver as defined by claim 5,  
wherein the serial transfer line is a transfer line in  
accordance with a USB standard.

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16. The RAM-incorporated driver as defined by claim 1,  
wherein the serial transfer line is a transfer line in  
accordance with an IEEE 1394 standard.

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17. The RAM-incorporated driver as defined by claim 2,  
wherein the serial transfer line is a transfer line in  
accordance with an IEEE 1394 standard.

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18. The RAM-incorporated driver as defined by claim 3,  
wherein the serial transfer line is a transfer line in  
accordance with an IEEE 1394 standard.

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19. The RAM-incorporated driver as defined by claim 4,  
wherein the serial transfer line is a transfer line in  
accordance with an IEEE 1394 standard.

20. The RAM-incorporated driver as defined by claim 5,  
wherein the serial transfer line is a transfer line in

accordance with an IEEE 1394 standard.

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21. A display unit comprising:

5 a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 1, which drives the plurality of first electrodes; and

10 a scanning driver for scanning and driving the plurality of second electrodes.

22. A display unit comprising:

15 a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 2, which drives the plurality of first electrodes; and

a scanning driver for scanning and driving the plurality of second electrodes.

20 23. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second;

25 the RAM-incorporated driver as defined by claim 3, which drives the plurality of first electrodes; and

a scanning driver for scanning and driving the plurality of second electrodes.

24. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 4, which drives the plurality of first electrodes; and

a scanning driver for scanning and driving the plurality of second electrodes.

25. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 5, which drives the plurality of first electrodes; and

a scanning driver for scanning and driving the plurality of second electrodes.

26. Electronic equipment comprising:

the display unit as defined by claim 21; and

an MPU which supplies the command, the still-image data, and the moving-image data to the display unit.